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Overview

The PVDM3 brings new rich-media capabilities to Cisco® Unified Communications Solutions. High-density voice connectivity. Conferencing, transcoding, and transrating. Management of voice quality. Power savings, secure voice, and more. The module resides on the Cisco 2900 and 3900 Series routers to enable these capabilities. The next-generation high-density packet voice digital-signal-processor (DSP) modules are available in six densities: PVDM3-16, PVDM3-32, PVDM3-64, PVDM3-128, PVDM3-192, and PVDM3-256. The modules have 16, 32, 64, 128, 192, and 256 channels, respectively.

Quick Specification

Product Code	PVDM3-32
Description	32-channel high-density voice DSP module
Maximum Number of Channels in Low-Complexity Codecs	32
Maximum Number of Channels in Medium-Complexity Codecs	21
Maximum Number of Channels in High-Complexity Codecs	14
Platform Support	Cisco 2901, 2911, 2921, 2951, 3925, 3945, 3925-E, and 3945-E Integrated Service Routers
Availability	Unified Communications License on Universal Cisco IOS Software image
Release	Cisco IOS Software Release 15.0(1)M

Product Details:

The Front Panel:



Accessories for PVDM3-32:

Compare to Similar Items:

Product Code	PVDM3-16	PVDM3-32	PVDM3-64	PVDM3-128	PVDM3-192	PVDM3-256
Description	16-channel high-density voice DSP module	32-channel high-density voice DSP module	64-channel high-density voice DSP module	128-channel high-density voice DSP module	192-channel high-density voice DSP module	256-channel high-density voice DSP module
Maximum Number of Channels in Low-Complexity Codecs	16	32	64	128	192	256
Maximum Number of Channels in Medium-Complexity Codecs	12	21	42	96	138	192
Maximum Number of Channels in High-Complexity Codecs	10	14	28	60	88	120

Get more information:

Do you have any question about the PVDM3-32?

Contact us now via e-mail: info@hi-network.com

Specific Data Sheet:

Product Number	PVDM3-32
Components	
DSP	Multicore DSP technology
DSP external memory	512-megabit double-data-rate (DDR) synchronous dynamic random access memory (SDRAM) for each DSP
Interface	240-pin dual-inline-memory-module (DIMM) interface
Features	
Echo cancellation	Software echo cancellation compliant with ITU-T G.168, with 128-ms tail coverage
Operating temperature	-41 to 131°F (-5 to 55°C)
Voltage	3.3V
Current	1.47A

PVDM3-32 Datasheet

Power	4.85W
Regulatory and Compliance	
Safety	<ul style="list-style-type: none"> Complies with IEC 60950 (worldwide) and AS/NZS 60950-1 (Australia and New Zealand), CAN/CSA-C22.2 No. 60950-1, 1st and 2nd Ed. (Canada), and GB4943-95 (People's Republic of China) EN60950-1, 1st and 2nd Ed. (CENELEC; includes EU and EFTA) NOM-019-SCFI-1998 (Mexico), and UL 60950-1, 1st and 2nd Ed. (United States)
Homologation	Platform dependent
Mean time between failure (MTBF)	System dependent
EMC	
Emissions	<ul style="list-style-type: none"> CISPR22, Class B EN55022, Class B, CFR47, Part 15, Subpart B, Class B
Harmonics	EN61000-3-2
Flicker	EN61000-3-3
Immunity	<ul style="list-style-type: none"> CISPR24 EN 55024 EN50082-1
Electrostatic discharge (ESD)	EN 61000-4-2
RF fields	EN 61000-4-3
EFT	EN 61000-4-4
Surge	EN 61000-4-5
Conducted RF	EN 61000-4-6
Power-frequency magnetic fields	EN 6100-4-8
Voltage dips, sags, and interruptions	EN 61000-4-11

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